

Fig 1

Hardware

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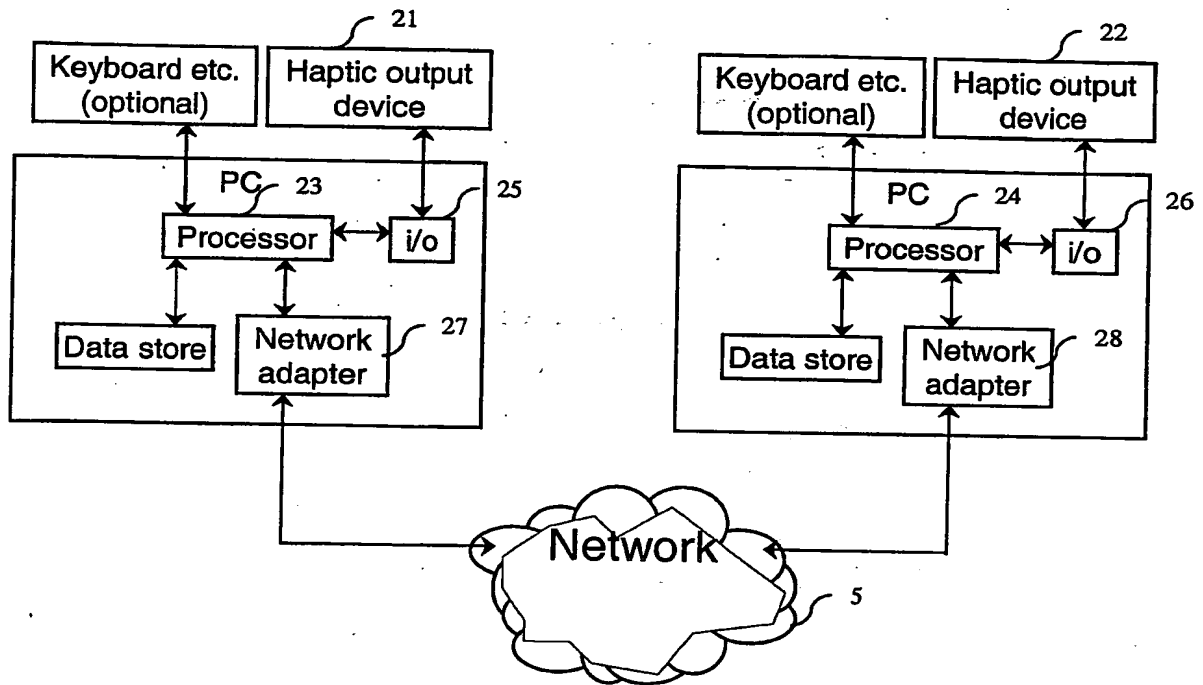
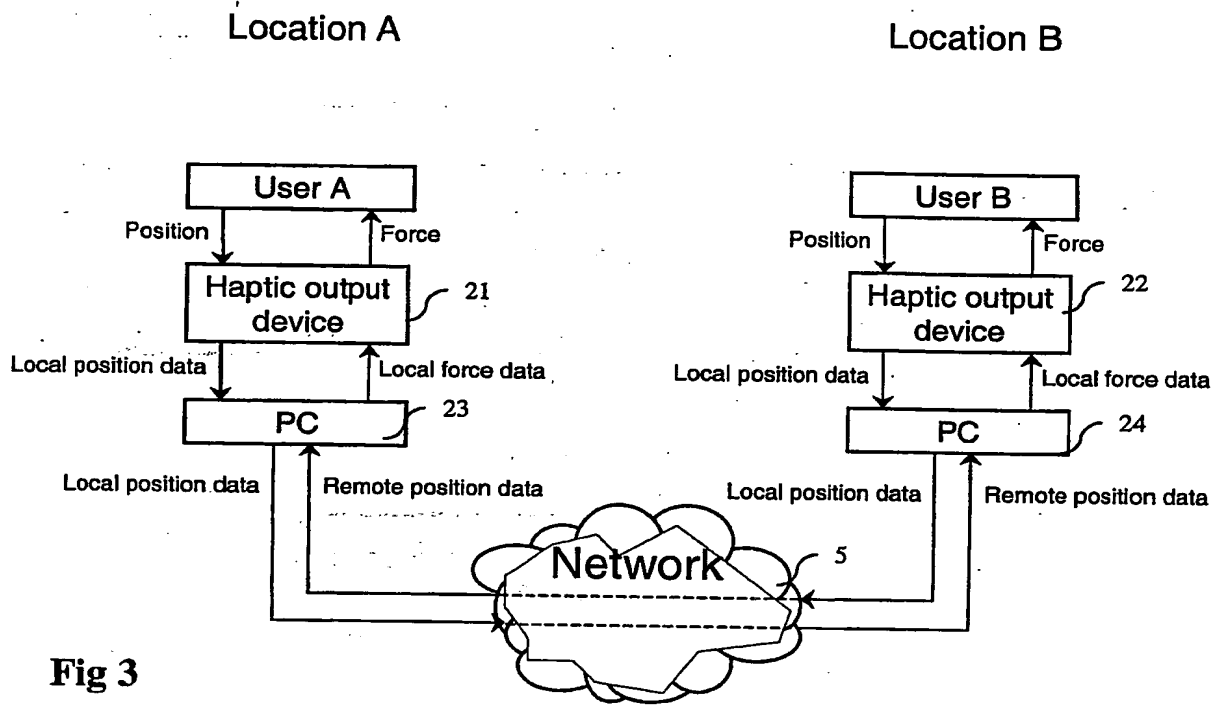
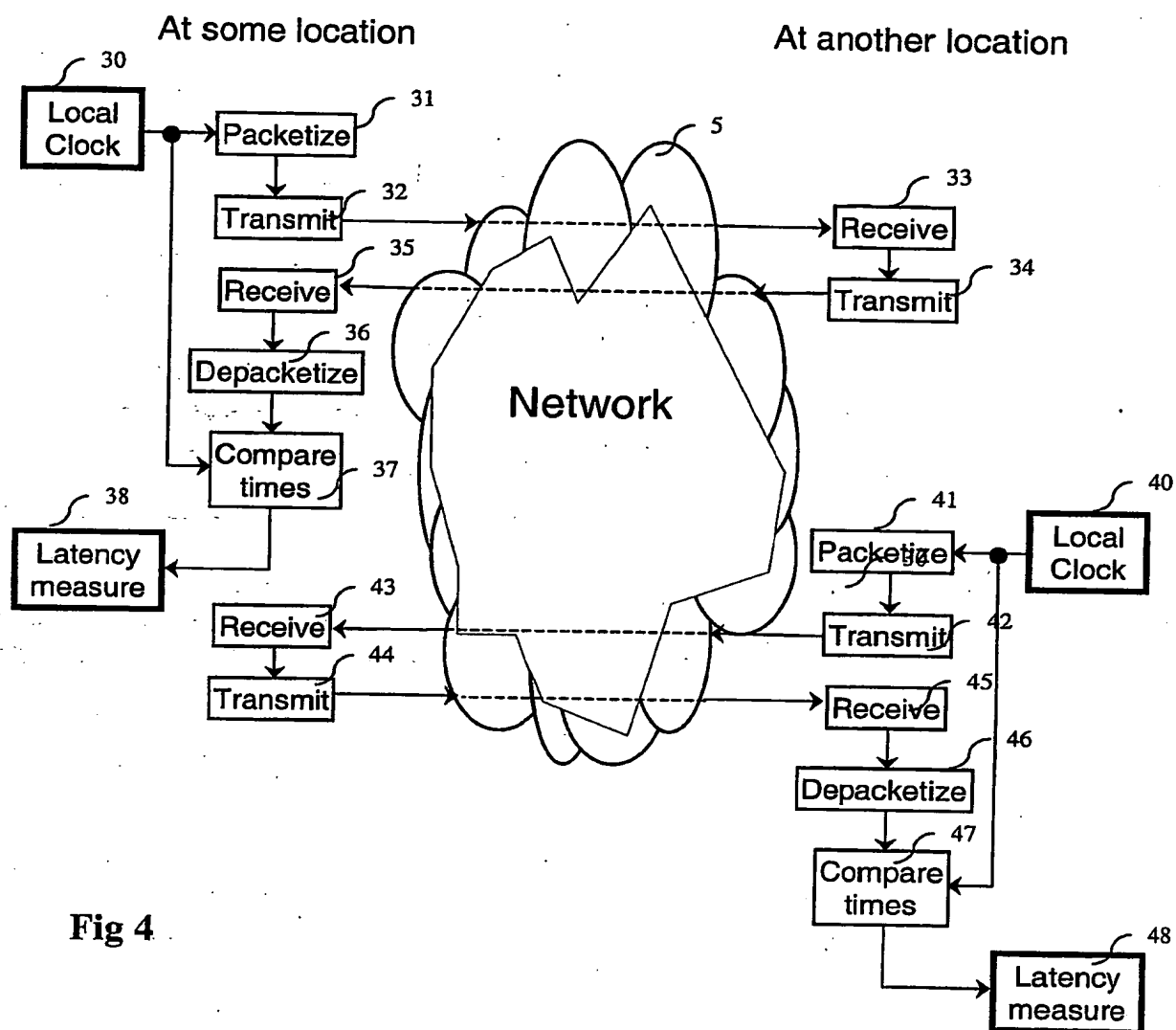


Fig 2

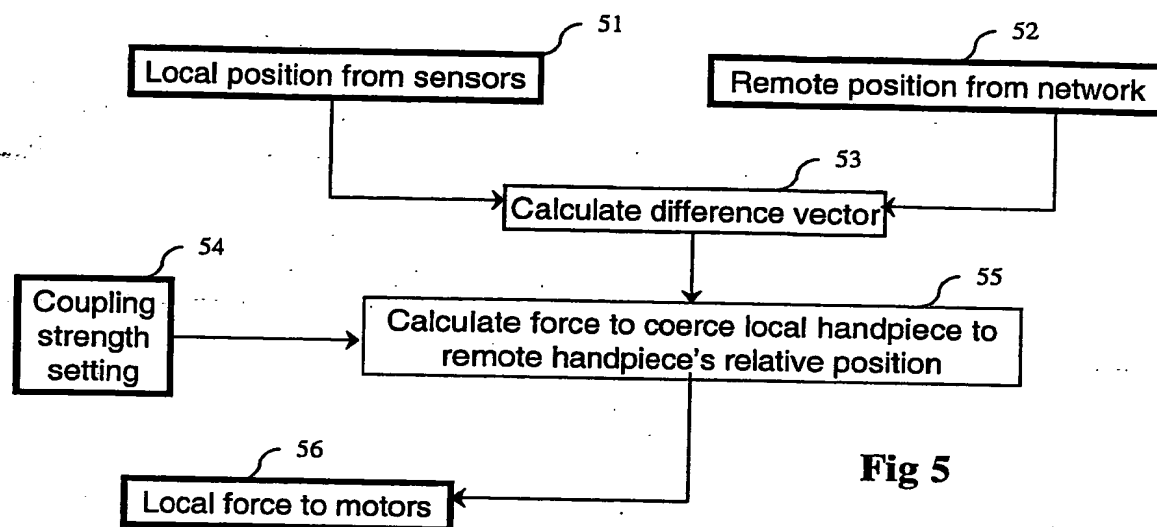
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A Method of Latency Measurement



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**Fig 5**

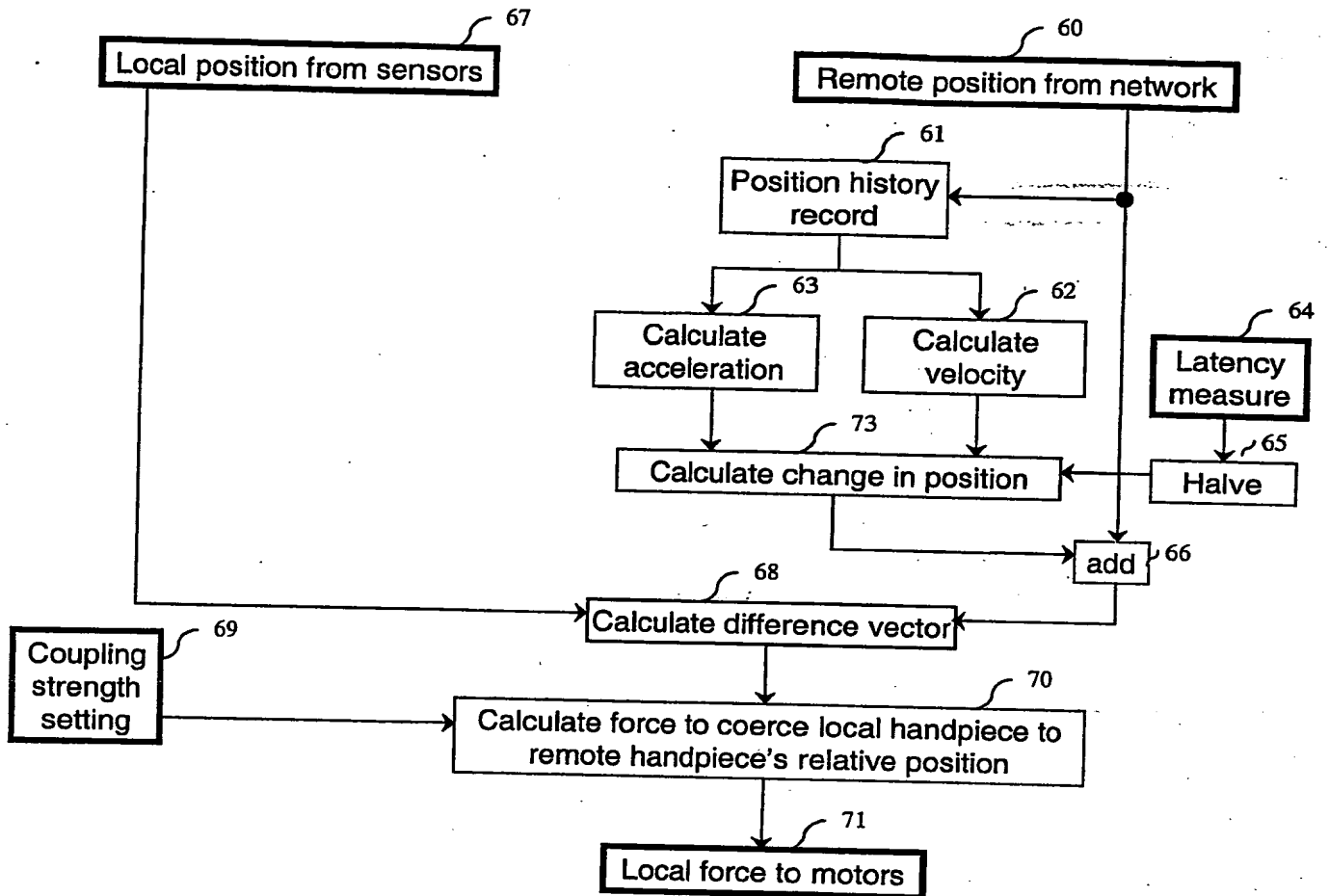


Fig 6

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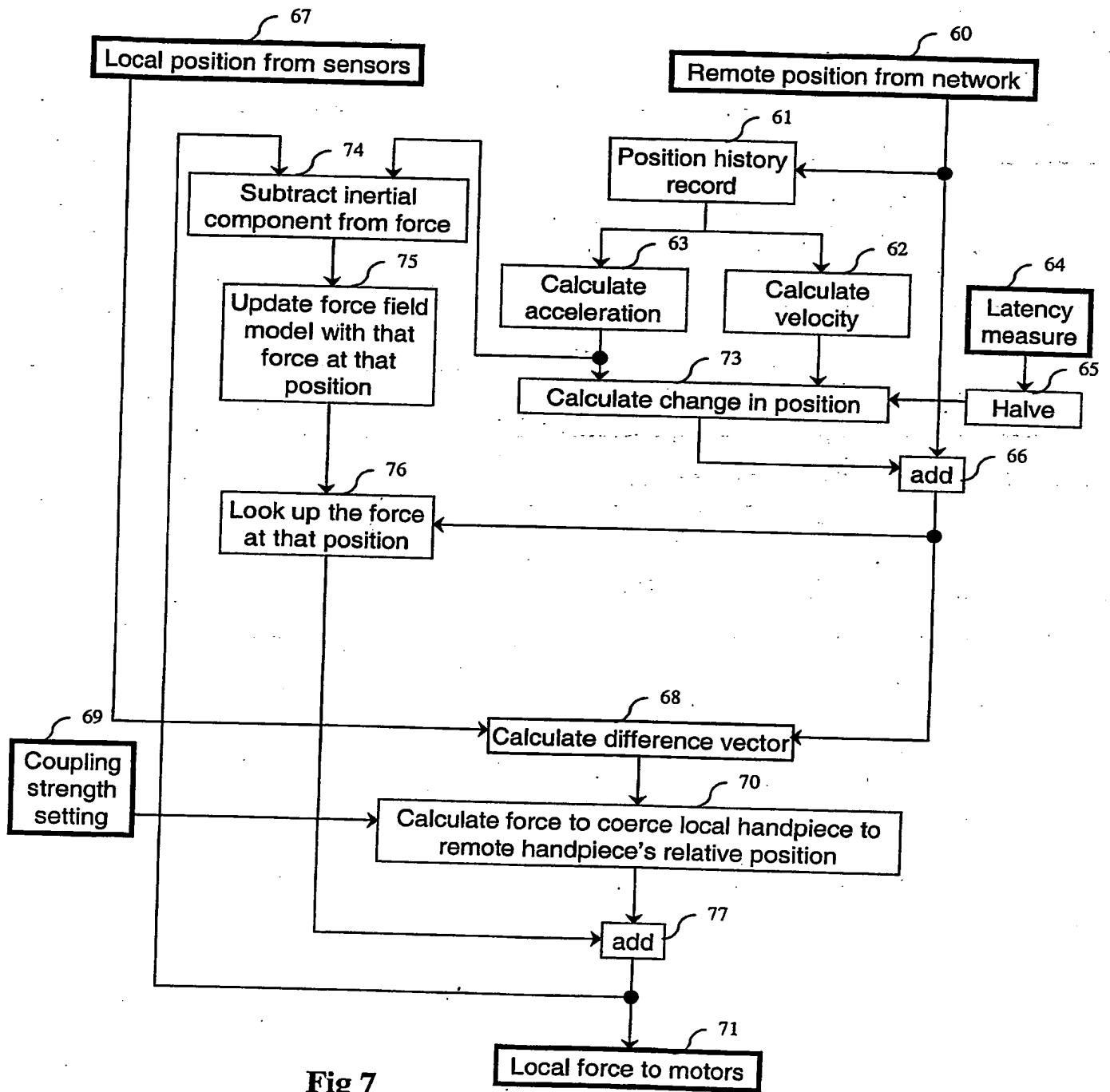


Fig 7

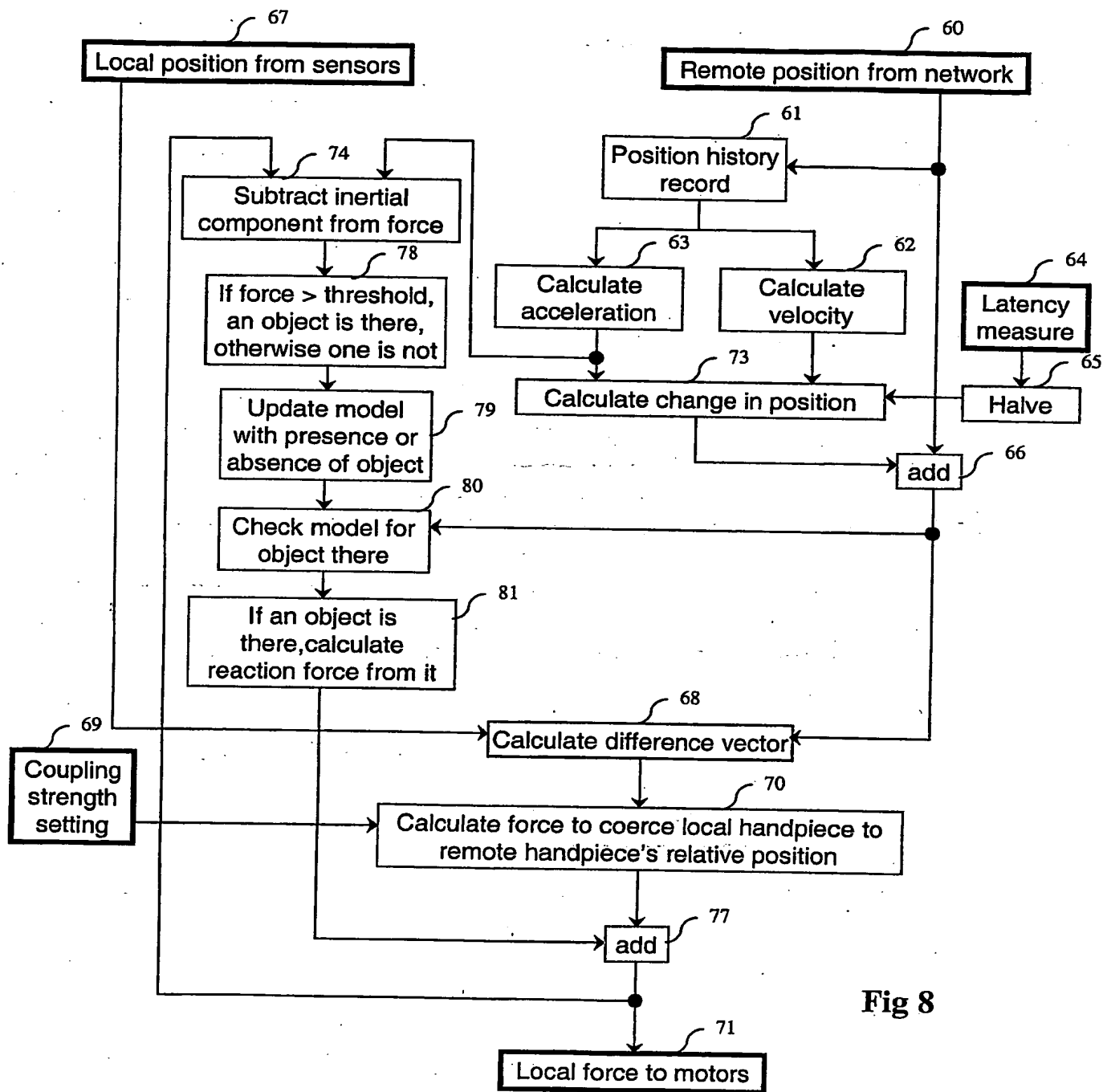


Fig 8